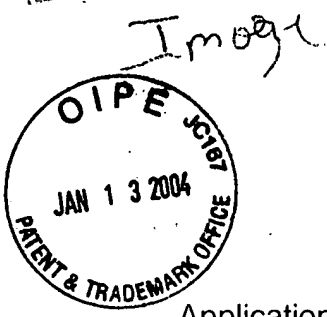


1765



MEMC 00-1400 (2806.1)
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of Robert J. Falster et al.
Serial No. 10/039,196
Filed January 2, 2002
Confirmation No. 7221

Art Unit 1765

For PROCESS FOR PREPARING SINGLE CRYSTAL SILICON HAVING
IMPROVED GATE OXIDE INTEGRITY

January 8, 2004

STATUS INQUIRY

TO THE COMMISSIONER FOR PATENTS,
SIR:

More than twenty-four (24) months have passed since the filing of this application on January 2, 2002. Accordingly, please advise the undersigned of the present status of this application.

Respectfully submitted,

Derick E. Allen, Reg. No. 43,468
SENNIGER, POWERS, LEAVITT & ROEDEL
One Metropolitan Square, 16th Floor
St. Louis, Missouri 63102
(314) 231-5400



MEMC 00-1400 (2806.1)
PATENT

CERTIFICATE OF MAILING

I certify that this Status Inquiry concerning application Serial No. 10/039,196 is being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this 8th day of January, 2004.

A handwritten signature in cursive script that reads "Sandra K. Dean".

Sandra K. Dean

DEA/skd